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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Woo Sik Yoo  
Assignee: WaferMasters, Inc.  
Title: INTEGRATED ASHING AND IMPLANT ANNEALING METHOD  
Serial No.: 10/690,315 Filing Date: October 20, 2003  
U.S. Patent No.: 6,897,162 Issue Date: May 24, 2005  
Examiner: Asok Kumar Sarkar Group Art Unit: 2812  
Docket No.: M-15271 US Confirmation No.: 4524

San Jose, California  
August 16, 2006

Attn: Certificate of Corrections Branch  
Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REQUEST FOR CERTIFICATE OF CORRECTION**

Sir:

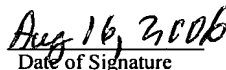
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The errors sought to be corrected were made by the Patent and Trademark Office. Thus, no fee is required for the Certificate of Correction pursuant to 37 C.F.R. § 1.322.

Should the U.S. Patent and Trademark Office believe that an additional fee is required please charge Deposit Account No. 50-2257, or has any inquiries concerning this request, please contact the undersigned Attorney for Applicant at (408) 392-9250.

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Attorney for Applicant(s)

  
Date of Signature

Respectfully submitted,



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**Certificate  
AUG 23 2006  
of Correction**

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## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO. : 6,897,162 B1

APPLICATION NO.: 10/690,315

ISSUE DATE : May 24, 2005

INVENTOR(S) : Woo Sik Yoo

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 7, line 2 delete "ate" and insert --are--

Column 7, line 7 delete "sing" and insert --processing--

### MAILING ADDRESS OF SENDER (Please do not use customer number below):

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